

Classroom lectures; Practical sessions to be carried out in groups of 2 to 3 students

Study material

Type: Slides

Name: Extensive set of detailed PowerPoint slides about 3 chapters that can be downloaded for free from the Ufora website

Indicative price: Free or paid by faculty

Optional: no

Language : English

Number of Slides : 260

Available on Ufora : Yes

Online Available : No

Available in the Library : No

Available through Student Association : No

References

- S.M. Sze, "VLSI technology", McGraw-Hill, New York 1988
- C.Y. Chang and S.M.Sze, "ULSI technology", McGraw-Hill, New York 1996
- C.F. Coombs, "Printed Circuits Handbook", McGraw-Hill, New York 1995
- R.L. Geiger, P.E. Allen, N.R. Strader, "VLSI design techniques for analog and digital circuits", McGraw-Hill, New York 1993

Course content-related study coaching

Continuous guidance/support, for the theoretical classes as well as for the design project, during the whole semester by the responsible professor and a scientific coworker.

Assessment moments

end-of-term and continuous assessment

Examination methods in case of periodic assessment during the first examination period

Oral assessment

Examination methods in case of periodic assessment during the second examination period

Oral assessment

Examination methods in case of permanent assessment

Assignment

Possibilities of retake in case of permanent assessment

examination during the second examination period is possible in modified form

Extra information on the examination methods

During examination period: oral closed-book examination.

Outside examination period: report of the IC design project that takes about 1 month time (schematic design + simulations + layout).

Calculation of the examination mark

Evaluation during examination period: 70%

Evaluation outside examination period: 30%