

- Design methodology for FPGAs

Final competences

To transform theoretical knowledge from other courses into practical applications.

Conditions for credit contract

Access to this course unit via a credit contract is determined after successful competences assessment

Conditions for exam contract

This course unit cannot be taken via an exam contract

Teaching methods**Learning materials and price**

Consumables must be foreseen for the realisation and testing of the hardware: PCB software design licences, PCB manufacturing cost, components, clean room materials, probe needles, Cost is strongly project dependent and is estimated to amount between 100 and 200 euro per project on average. This will be paid by the research groups.

References**Course content-related study coaching****Assessment moments**

continuous assessment

Examination methods in case of periodic assessment during the first examination period**Examination methods in case of periodic assessment during the second examination period****Examination methods in case of permanent assessment**

Assignment

Possibilities of retake in case of permanent assessment

examination during the second examination period is possible in modified form

Extra information on the examination methods

During semester: graded project reports; graded oral presentation. Frequency: every week.

Calculation of the examination mark