



- Design methodology for FPGAs

**Final competences**

To transform theoretical knowledge from other courses into practical applications.

**Conditions for credit contract**

Access to this course unit via a credit contract is determined after successful competences assessment

**Conditions for exam contract**

This course unit cannot be taken via an exam contract

**Teaching methods****Study material**

Type: Project

Name: Project on Hardware design work in groups

Indicative price: Free or paid by faculty

Optional: no

**References****Course content-related study coaching****Assessment moments**

continuous assessment

**Examination methods in case of periodic assessment during the first examination period****Examination methods in case of periodic assessment during the second examination period****Examination methods in case of permanent assessment**

Assignment

**Possibilities of retake in case of permanent assessment**

examination during the second examination period is possible in modified form

**Extra information on the examination methods**

During semester: graded project reports; graded oral presentation. Frequency: every week.

**Calculation of the examination mark**